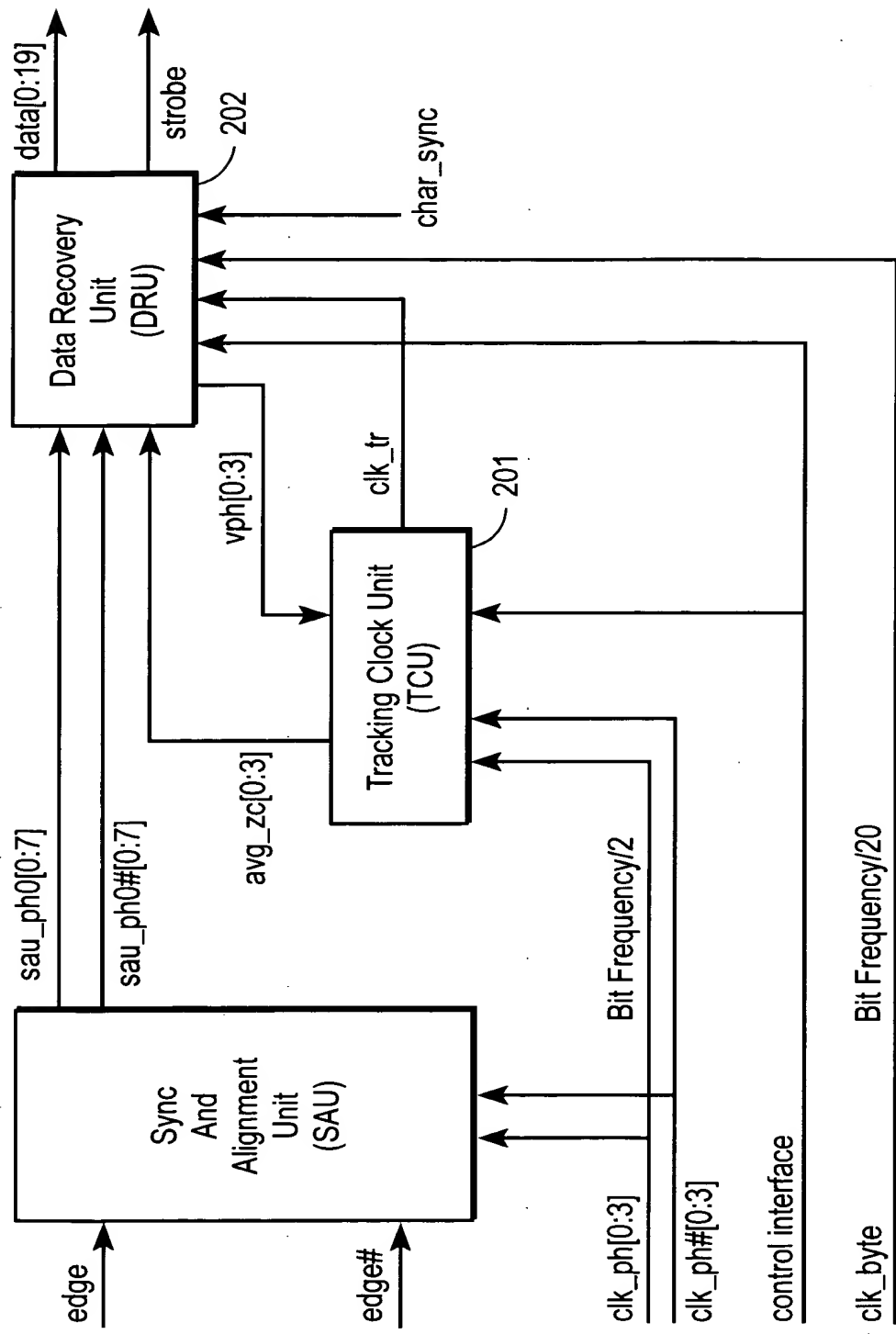


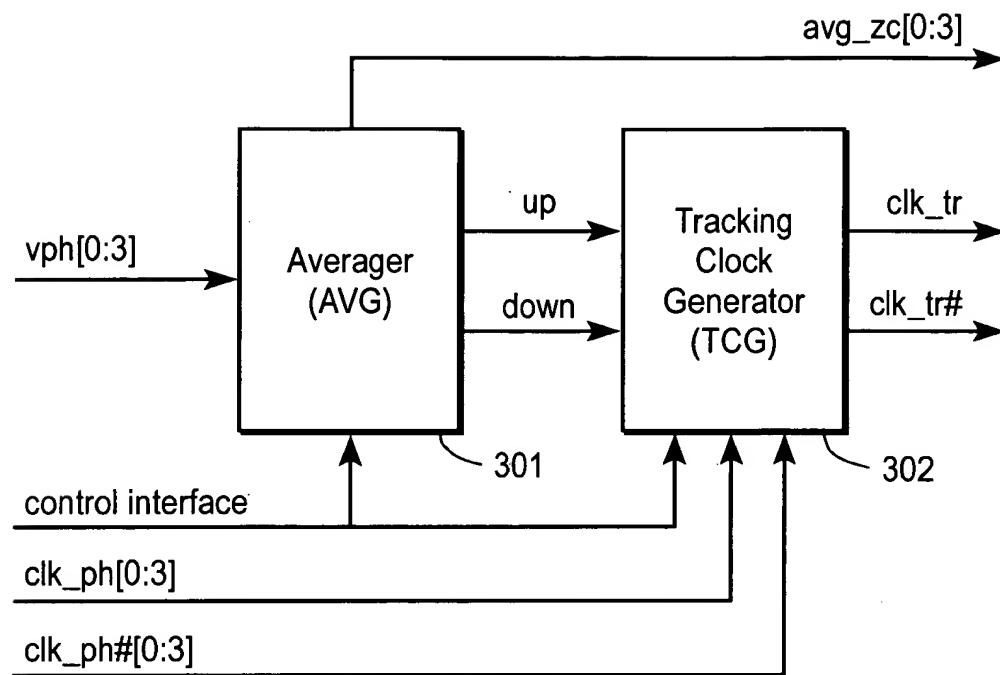
High Level Block Diagram of Edge Based Receiver for 2.5Gb/s Operation

FIG. 1



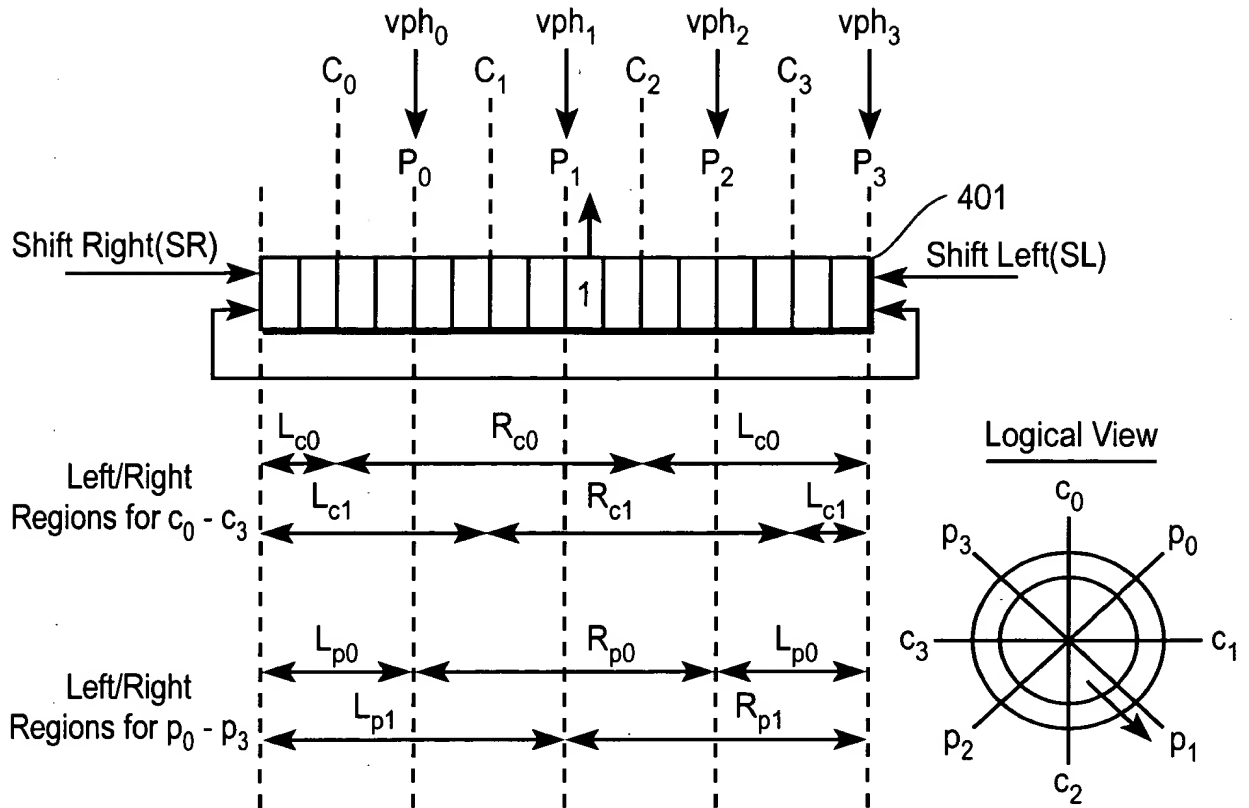
Edge Processing Block Diagram

FIG. 2



Tracking Clock Unit (TCU) Block Diagram

FIG. 3



Legend:

p_i - phase i position

L_{pi} - region left of p_i

L_{ci} - region left of c_i

c_i - phase i center

R_{pi} - region right of p_i

R_{ci} - region right of c_i

Barrel Shifter for Phase Tracking

FIG. 4

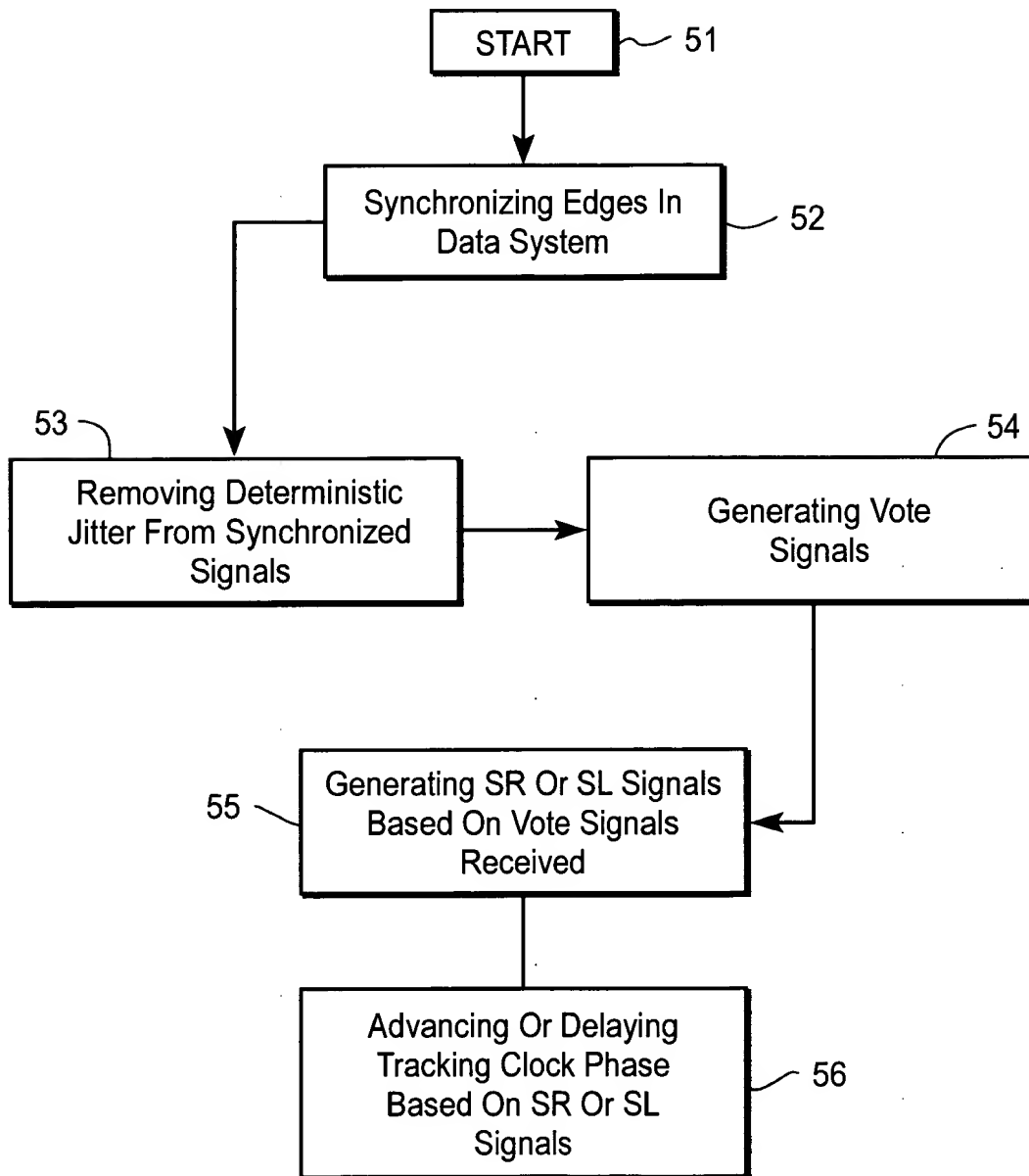


FIG. 5

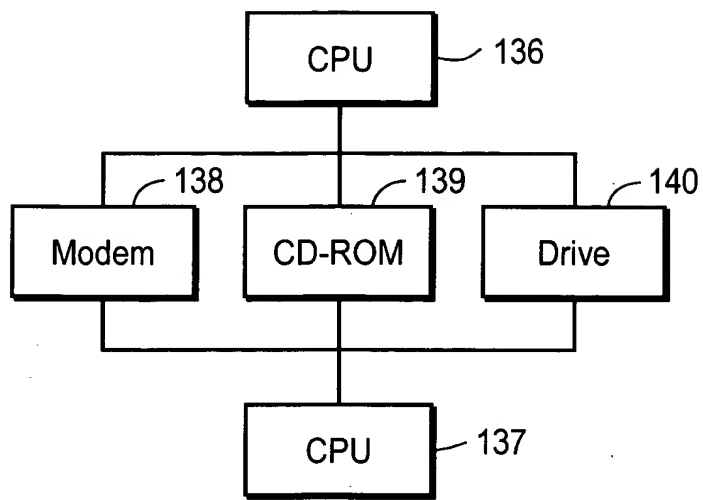


FIG. 6a

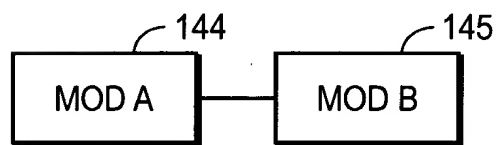


FIG. 6c

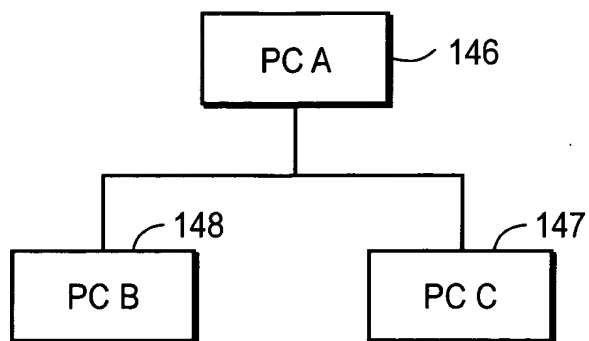


FIG. 6d

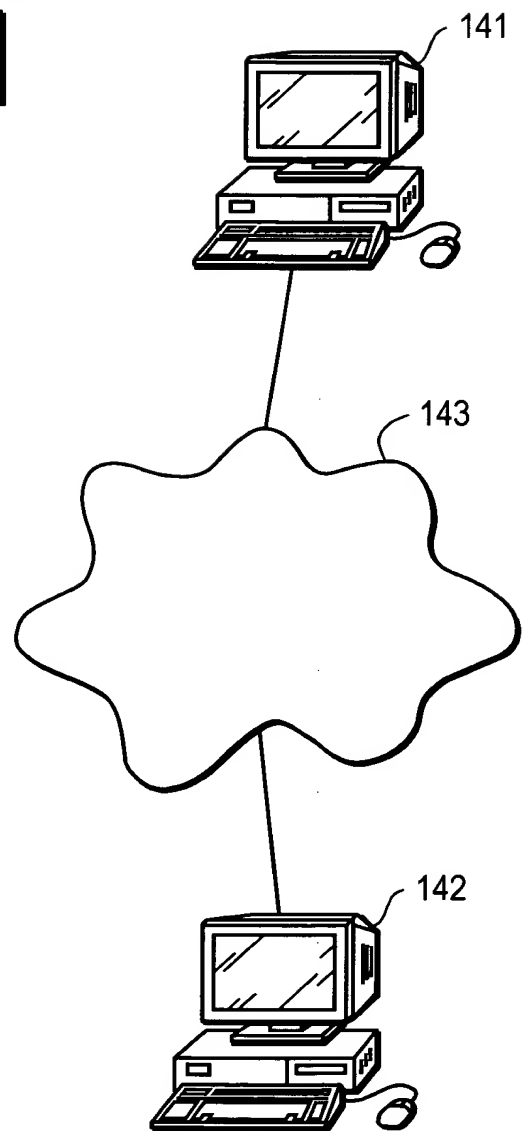


FIG. 6b